



US 20150084843A1

(19) **United States**(12) **Patent Application Publication**
Li et al.(10) **Pub. No.: US 2015/0084843 A1**(43) **Pub. Date: Mar. 26, 2015**(54) **PIXEL DRIVING CIRCUIT, DRIVING
METHOD THEREOF AND DISPLAY
APPARATUS****Publication Classification**

(51) **Int. Cl.**
G09G 3/32 (2006.01)
(52) **U.S. Cl.**
CPC **G09G 3/3258** (2013.01); **G09G 2330/00**
(2013.01)
USPC **345/76**

(71) Applicants: **BOE TECHNOLOGY GROUP CO.,
LTD**, Beijing (CN); **CHENGDU BOE
OPTOELECTRONICS
TECHNOLOGY CO., LTD.**, Chengdu
(CN)(72) Inventors: **Tianma Li**, Beijing (CN); **Fan Li**,
Beijing (CN)(21) Appl. No.: **14/368,408**(22) PCT Filed: **Oct. 18, 2013**(86) PCT No.: **PCT/CN2013/085516**

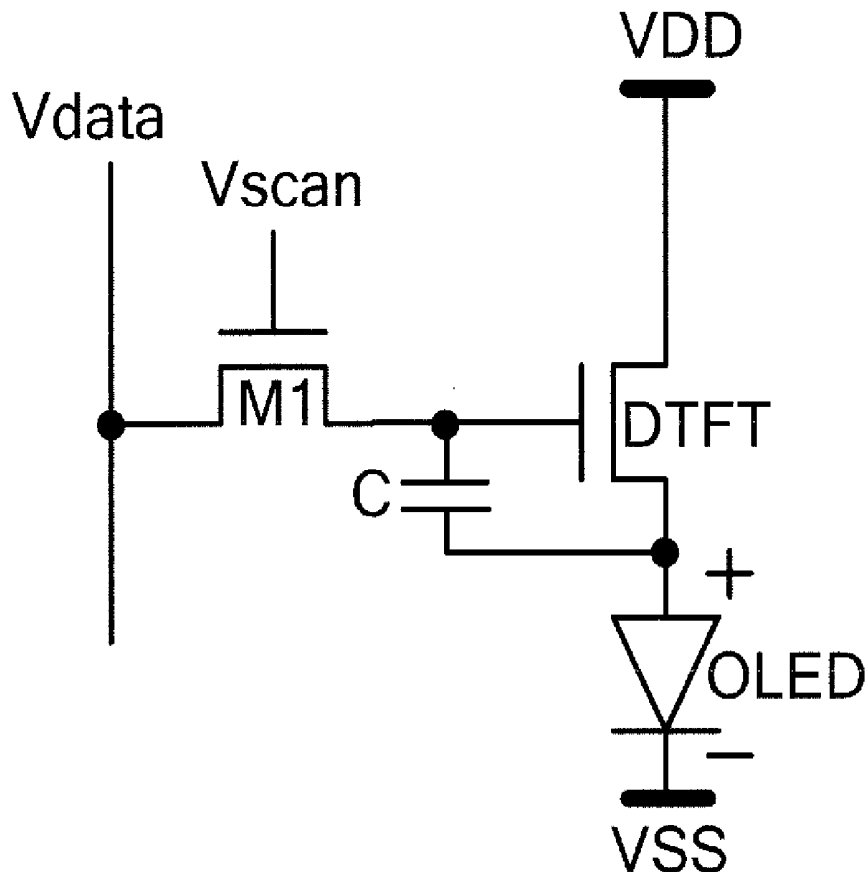
§ 371 (c)(1),

(2) Date: **Jun. 24, 2014**(30) **Foreign Application Priority Data**

Jul. 31, 2013 (CN) 201310329899.7

(57) **ABSTRACT**

Provided are a pixel driving circuit, a driving method thereof and a display apparatus. The pixel driving circuit includes an electroluminescent element, a driving transistor, a switch unit, a charging unit, a compensation unit, a cutoff unit and a storage unit. The pixel driving circuit charges the storage capacitor by the charging unit first, and then uses the method of charging first and discharging later to make the voltage in the storage capacitor match the driving current quickly to perform fast compensation of the threshold voltage V_{th} , and thus make the driving current provided by the driving transistor not influenced by the threshold voltage V_{th} , thereby raising the consistency of the driving currents and improving the uniformity of brightness.



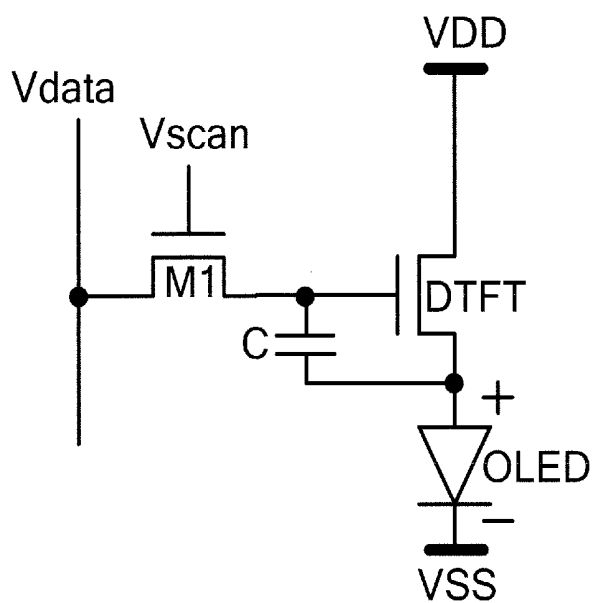


Fig.1

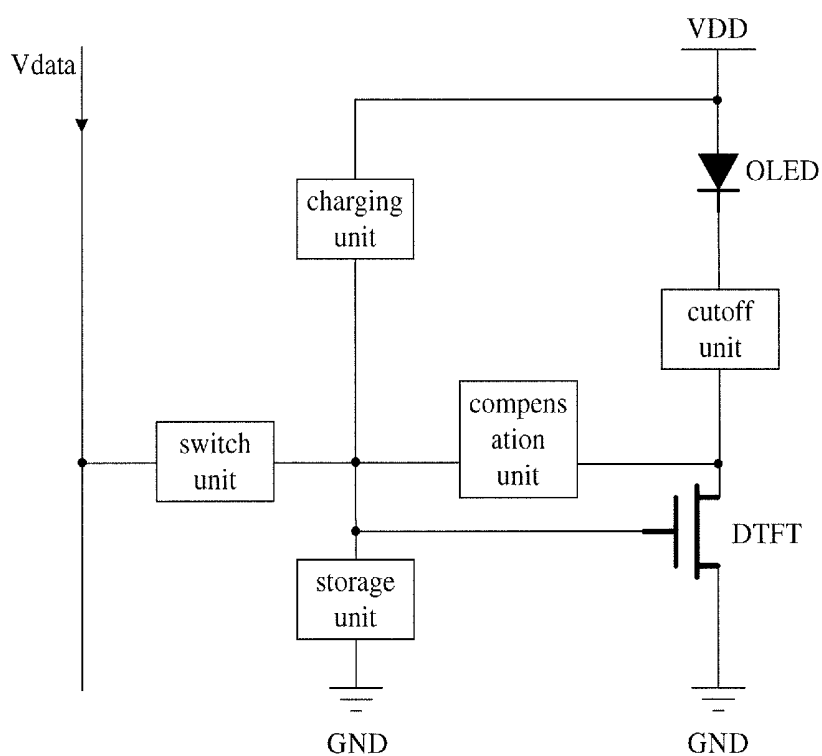


Fig.2

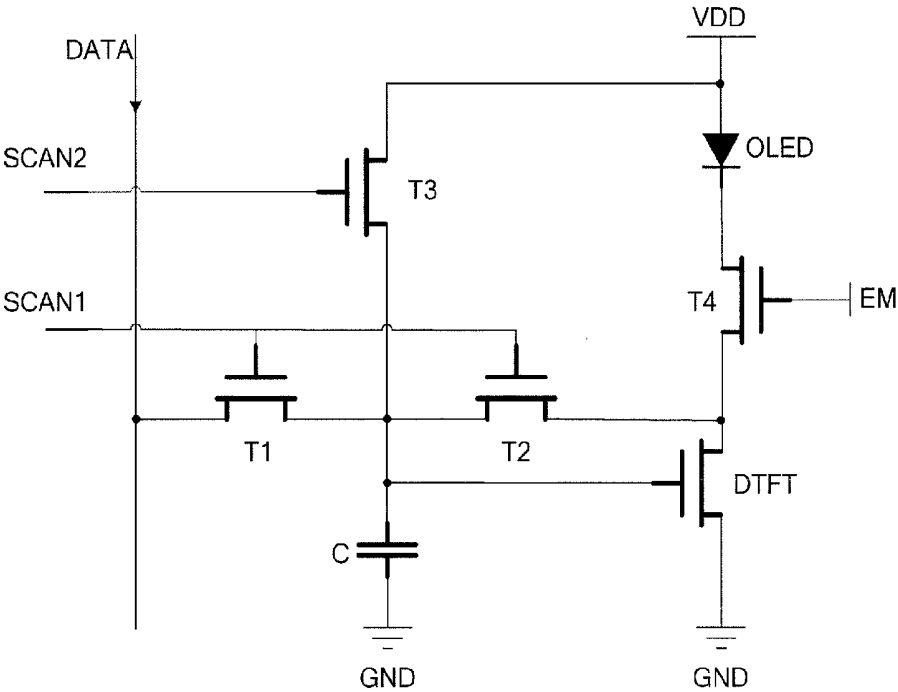


Fig.3

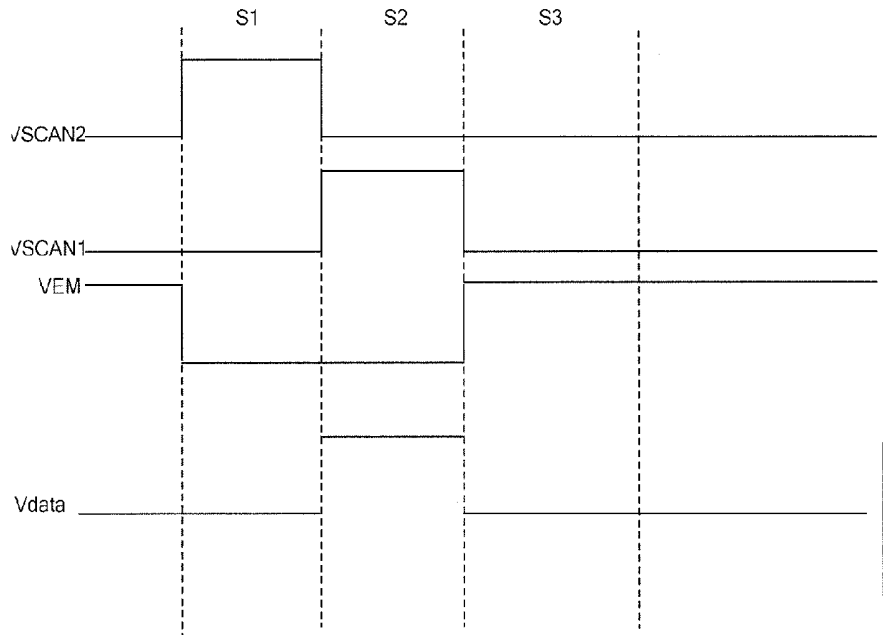


Fig.4

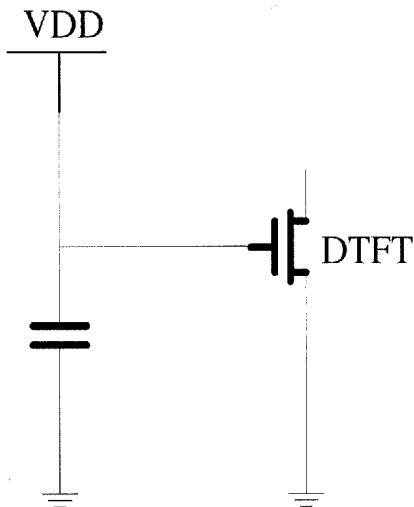


Fig.5

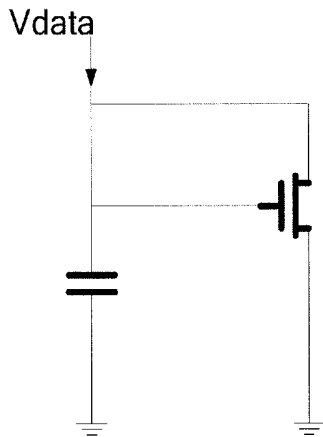


Fig.6

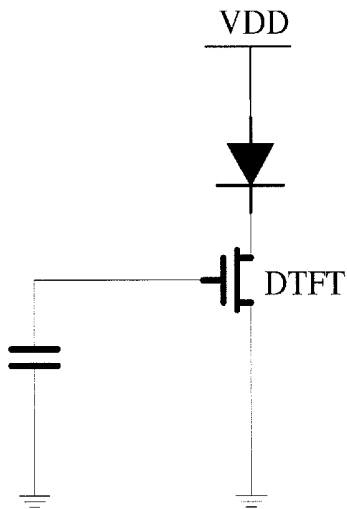


Fig.7

PIXEL DRIVING CIRCUIT, DRIVING METHOD THEREOF AND DISPLAY APPARATUS

TECHNICAL FIELD OF THE DISCLOSURE

[0001] The present disclosure relates to the technical field of organic light-emitting display, and particularly to a pixel driving circuit, a driving method thereof and a display apparatus.

BACKGROUND

[0002] Compared with traditional liquid crystal panels, the Active Matrix/Organic Light Emitting Diode (AMOLED) display panel has characteristics such as faster response, larger contrast, wider angle of view, and so on, and thus has drawn more and more attention to display technology developers.

[0003] The AMOLED display panel can emit light due to being driven by current generated by a driving Thin Film Transistor (TFT) in the saturation state. Different currents would be generated due to different threshold voltages when the same gray-scale voltage is input, which results in nonuniformity of driving current. The Low Temperature Poly-Silicon (LTPS) has very poor uniformity of threshold voltage V_{th} in the manufacturing procedure, and the threshold voltage V_{th} also drifts; therefore, conventional 2T1C circuits have very poor uniformity of brightness.

[0004] FIG. 1 illustrates a voltage driven pixel driving circuit structure (2T1C) composed of 2 TFT transistors and 1 capacitors as known. The first transistor M1 transports the voltage on the data line to the gate of the driving transistor DTFT which transforms this data voltage into corresponding current to be supplied to the organic light emitting diode. During normal operation, the driving transistor DTFT should operate at the saturation region to supply stable driving current within the scanning period of one row. The driving current (i.e., the current flowing through the OLED) can be expressed as:

$$I_{OLED} = \frac{1}{2} \mu_n \cdot C_{ox} \cdot \frac{W}{L} \cdot (V_{data} - V_{oled} - V_{th})^2$$

wherein μ_n is the carrier mobility, C_{ox} is the capacitance of the gate oxide layer, W/L is the aspect ratio of the transistor, V_{data} is the data voltage, V_{oled} is the operating voltage of the OLED which is shared by all pixel units, and V_{th} is the threshold voltage of the driving transistor DTFT. As can be seen from the above expression, if V_{th} is different between different pixel units, it results in that the driving current would be different; if the threshold voltage V_{th} of the driving transistor drifts with time, it may results that current at former time would be different from that at latter time, which causes afterimage, reduces display effect, and influences users' experience.

SUMMARY

[0005] A technical problem to be solved by the present disclosure is to provide a pixel driving circuit and a driving method thereof in view of the drawbacks of the prior art, in which the driving current provided by the driving transistor would not be influenced by the threshold voltage V_{th} of the driving transistor by compensating the threshold voltage V_{th}

in order to improve the consistency of the driving current and the uniformity of the brightness.

[0006] A solution according to an aspect of the present disclosure is a pixel driving circuit comprising an electroluminescent element, a driving transistor, a switch unit, a charging unit, a compensation unit, a cutoff unit and a storage unit, wherein

[0007] the switch unit is configured to control data voltage writing of a data line, a first terminal of the switch unit is connected to the data line, and a second terminal of the switch unit is connected to a first terminal of the charging unit, a first terminal of the compensation unit, a first terminal of the storage unit and the gate of the driving transistor respectively;

[0008] the charging unit is configured to control a driving power source to charge the storage unit, and a second terminal of the charging unit is connected to the driving power source and a first terminal of the electroluminescent element respectively;

[0009] the cutoff unit is configured to cut off electric connection between the electroluminescent element and the source of the driving transistor, a first terminal of the cutoff unit is connected to a second terminal of the electroluminescent element, and a second terminal of the cutoff unit is connected to the source of the driving transistor;

[0010] the compensation unit is configured to pre-store a threshold voltage of the driving transistor into the storage unit, and a second terminal of the compensation unit is connected to the source of the driving transistor; and

[0011] the drain of the driving transistor and a second terminal of the storage unit are grounded.

[0012] As an example, the electroluminescent element is an organic light emitting diode (OLED), the storage unit is a storage capacitor, the switch unit is a first transistor, the charging unit is a third transistor, the compensation unit is a second transistor, and the cutoff unit is a fourth transistor;

[0013] the gate of the first transistor is connected to a first scan line, the source of the first transistor is connected to the data line, and the drain of the first transistor is connected to the first terminal of the storage capacitor, the source of the second transistor, the drain of the third transistor and the gate of the driving transistor respectively;

[0014] the gate of the third transistor is connected to a second scan line, and the source of the third transistor is connected to the driving power source and the first terminal of the OLED respectively;

[0015] the gate of the fourth transistor is connected to a control signal line, the source of the fourth transistor is connected to the second terminal of the OLED, and the drain of the fourth transistor is connected to the source of the driving transistor and the drain of the second transistor respectively; and

[0016] the gate of the second transistor is connected to the first scan line.

[0017] As an example, all the transistors have a same channel type.

[0018] As an example, all the transistors are N channel thin film transistors (TFTs); the first terminal of the OLED is the anode of the OLED, the anode of the OLED is connected to the high level output terminal of the driving power source, and the second terminal of the OLED is the cathode of the OLED. As an example, all the transistors are P channel thin film transistors (TFTs); the first terminal of the OLED is the cathode of the OLED, the cathode of the OLED is connected to the

low level output terminal of the driving power source, and the second terminal of the OLED is the anode of the OLED.

[0019] The present disclosure also provides a driving method for driving any pixel driving circuit as described in the above, comprising the following steps of:

[0020] applying a control signal at a second scan line to control a third transistor to be turned on, applying a control signal at a first scan line and a control signal line to control a first transistor, a second transistor and a fourth transistor to be turned off, and a driving power source charging a storage capacitor;

[0021] applying a control signal at the first scan line to control the first transistor and the second transistor to be turned on, applying a control signal at the second scan line to control the third transistor to be turned off, and writing a threshold voltage of a driving transistor and a data voltage at a data line into the storage capacitor; and

[0022] applying a control signal at a control signal line to turn on the fourth transistor, applying a control signal at the first scan line to control the first transistor and the second transistor to be turned off, and driving the OLED to emit light by voltage stored in the storage capacitor.

[0023] The present disclosure also provides a display apparatus comprising any pixel driving circuit as described in the above.

[0024] The pixel driving circuit provided in an embodiment of the present disclosure charges the storage capacitor by the charging unit first, and then uses the method of charging first and discharging later to make the voltage in the storage capacitor match the driving current quickly to perform fast compensation of the threshold voltage V_{th} , and thus make the driving current provided by the driving transistor not influenced by the threshold voltage V_{th} , thereby raising the consistency of the driving currents, improving the uniformity of brightness and enhancing users' experience.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] FIG. 1 is a schematic structural diagram of the known 2T1C pixel driving circuit;

[0026] FIG. 2 is a schematic structural diagram of a pixel driving circuit in a first embodiment of the present disclosure;

[0027] FIG. 3 is a schematic diagram of a specific implementation of the pixel driving circuit in FIG. 2;

[0028] FIG. 4 is a driving sequence diagram of the pixel driving circuit in FIG. 3;

[0029] FIG. 5 is a schematic structural diagram of the equivalent circuit of the pixel driving circuit in FIG. 3 at stage S1;

[0030] FIG. 6 is a schematic structural diagram of the equivalent circuit of the pixel driving circuit in FIG. 3 at stage S2;

[0031] FIG. 7 is a schematic structural diagram of the equivalent circuit of the pixel driving circuit in FIG. 3 at stage S3.

DETAILED DESCRIPTION

[0032] In the following, specific implementations of the present disclosure will be described further in connection with drawings and embodiments. The following embodiments are only for illustrating the present disclosure, but not for limiting the scope of the present disclosure.

First Embodiment

[0033] As shown in FIG. 2, a pixel driving circuit provided in an embodiment of the present disclosure mainly comprises an electroluminescent element (e.g. OLED shown in the figure), a driving transistor DTFT, a switch unit, a charging unit, a compensation unit, a cutoff unit and a storage unit. The switch unit is configured to control data voltage writing of a data line, a first terminal of the switch unit is connected to the data line, and a second terminal of the switch unit is connected to a first terminal of the charging unit, a first terminal of the compensation unit, a first terminal of the storage unit and the gate of the driving transistor DTFT respectively. The charging unit is configured to control a driving power source to charge the storage unit, and a second terminal of the charging unit is connected to the driving power source and a first terminal of the electroluminescent element respectively. The cutoff unit is configured to cut off electric connection between the electroluminescent element and the source of the driving transistor DTFT, a first terminal of the cutoff unit is connected to a second terminal of the electroluminescent element, and a second terminal of the cutoff unit is connected to the source of the driving transistor DTFT. The compensation unit is configured to pre-store a threshold voltage of the driving transistor DTFT into the storage unit, and a second terminal of the compensation unit is connected to the source of the driving transistor DTFT. The drain of the driving transistor DTFT and a second terminal of the storage unit are grounded. The storage unit is charged by the charging unit first, and then a method of charging first and discharging later is used to make the voltage in the storage unit match the driving current quickly to perform fast compensation of the threshold voltage V_{th} , and thus makes the driving current provided by the driving transistor DTFT not influenced by the threshold voltage V_{th} , thereby raising the consistency of the driving currents, improving the uniformity of brightness and enhancing users' experience.

[0034] As shown in FIG. 3, the present embodiment provides an implementation of the above pixel driving circuit, comprising the driving transistor DTFT and the storage capacitor C. The electroluminescent element is an organic light emitting diode (OLED), the switch unit is a first transistor T1, the charging unit is a third transistor T3, the compensation unit is a second transistor T2, and the cutoff unit is a fourth transistor T4. The implementation also comprises a driving power source and a grounded terminal. A first scan line SCAN1 provides a control signal VSCAN1 to turn on or off the first transistor T1 and the second transistor T2. A second scan line SCAN2 provides a control signal VSCAN2 to turn on or off the third transistor T3. A control signal line EM provides a control signal VEM to turn on or off the fourth transistor T4. A data line DATA writes a data voltage signal Vdata into a pixel through the first transistor T1. The driving power source can output a high voltage VDD or a low voltage VSS.

[0035] In the present embodiment, the gate of the first transistor T1 is connected to the first scan line SCAN1, the source of the first transistor T1 is connected to the data line DATA, and the drain of the first transistor T1 is connected to the first terminal of the storage capacitor C, the source of the second transistor T2, the drain of the third transistor T3 and the gate of the driving transistor DTFT respectively. Under the control of the control signal provided by the first scan line SCAN1, the first transistor T1 provides the data voltage signal Vdata of the data line DATA to the storage capacitor C and the voltage

is maintained by the storage capacitor C. The gate of the third transistor T3 is connected to the second scan line SCAN2, and the source of the third transistor T3 is connected to the driving power source and the first terminal of the OLED respectively. The driving power source charges the storage capacitor C through the third transistor T3. The gate of the fourth transistor T4 is connected to a control signal line EM, the source of the fourth transistor T4 is connected to the second terminal of the OLED, and the drain of the fourth transistor T4 is connected to the source of the driving transistor DTFT and the drain of the second transistor T2 respectively. The gate of the second transistor T2 is connected to the first scan line. The drain of the driving transistor DTFT and a second terminal of the storage capacitor C are grounded. Under the control of the control signal provided by the first scan line, the second transistor T2 is turned on, and the gate and the source of the driving transistor DTFT are connected to form a diode connection to ensure the driving transistor DTFT operates at the saturation current region, causing the storage capacitor C to discharge until the voltages into the gate and the source of the driving transistor DTFT are both the data voltage at the data line. At this point, the voltage of the storage capacitor C is

$$V_c = \sqrt{\frac{2V_{data}}{K}} + V_{th},$$

where K is a constant related to the process and driving design, V_{data} is a voltage inputted by the data line. It can be seen that the threshold voltage of the driving transistor DTFT has been pre-stored into the storage capacitor C, achieving the purpose of compensating the threshold voltage. Other advantages of the pixel driving circuit of the present embodiment are that transistors of the same channel type are used (i.e. all transistors are N channel transistors) to reduce the complexity and fabrication cost of the manufacturing process, and the top light-emitting manner of OLED has higher pixel aperture ratio than the bottom light-emitting manner. However, it can be easily obtained by those skilled in the art that the pixel driving circuit provided in the embodiment of the present disclosure can be easily changed to a pixel driving circuit with only P channel transistors, which differs from the N channel thin film transistor (TFT) circuit in term of connection structures mainly in that when the transistors are all N channel TFTs, the first terminal of the OLED is the anode of the OLED, the anode of the OLED is connected to the high level output terminal of the driving power source, the second terminal of the OLED is the cathode of the OLED, and the cathode of the OLED is connected to the source of the fourth transistor T4; when the transistors are all P channel TFTs, the first terminal of the OLED is the cathode of the OLED, the cathode of the OLED is connected to the low level output terminal of the driving power source, the second terminal of the OLED is the anode of the OLED, and the anode of the OLED is connected to the source of the fourth transistor T4. Of course, the pixel driving circuit provided by an embodiment of the present disclosure can be easily changed to a CMOS (Complementary Metal Oxide Semiconductor) circuit or the like; it is not limited to the pixel driving circuit provided in the present embodiment, and will not be described herein to avoid redundancy.

[0036] An embodiment of the present disclosure also provides a driving method for driving the above mentioned pixel

circuit, mainly comprising a pre-charging time sequence period S1, a compensation time sequence period S2 and a driving-display time sequence period S3. In the following, description is made by taking a driving method applied to the pixel driving circuit shown in FIG. 3 as an example. All the transistors are N channel TFTs, and the schematic driving time sequence diagram is as shown in FIG. 4. The time sequence diagram schematically shows the changes of the control signal VSCAN1 of the first scan line SCAN1, the control signal VSCAN2 of the second scan line SCAN2, the data voltage Vdata of the data line DATA, and the control signal VEM of the control signal line EM in an operation time sequence of one frame. In the following, the operation procedures of the driving method of the pixel driving circuit according to an embodiment of the present disclosure in respective time sequence periods will be described in detail in connection with FIGS. 5-7 respectively.

Pre-Charging Time Sequence Period S1

[0037] The equivalent circuit diagram in this time sequence period is as shown in FIG. 5. In this time sequence period, a control signal of high level is applied at the second scan line to turn on the third transistor T3, a control signal of low level is applied at the first scan line and the control signal line to turn off the first transistor T1, the second transistor T2 and the fourth transistor T4, and the driving power source charges the storage capacitor C.

Compensation Time Sequence Period S2

[0038] The equivalent circuit diagram in this time sequence period is as shown in FIG. 6. In this time sequence period, a control signal of high level is applied at the first scan line to turn on the first transistor T1 and the second transistor T2. A control signal of low level is applied at the second scan line to turn off the third transistor T3 so as to make the gate and source of the driving transistor DTFT connected and make the driving transistor enter the saturation state, causing the storage capacitor C to discharge until both the gate voltage and the source voltage flowing into the driving transistor DTFT are the data voltage of the data line. At this point, the voltage of the storage capacitor C is

$$V_c = \sqrt{\frac{2V_{data}}{K}} + V_{th}$$

where K is a constant related to the process and driving design. The threshold voltage of the driving transistor DTFT and the data voltage at the data line are written into the storage capacitor C.

Driving-Display Time Sequence Period S3

[0039] The equivalent circuit diagram in this time sequence period is as shown in FIG. 7. In this time sequence period, a control signal of high level is applied at the control signal line to turn on the fourth transistor T4, and a control signal of low level is applied at the first scan line to turn off the first transistor T1 and the second transistor T2. Since the voltage VDD of the driving power source is larger than $V_{gs} = V_G - V_{th}$, where V_{gs} is the gate-source voltage of the driving transistor DTFT, and V_G is the gate potential of the driving transistor

DTFT, the driving transistor DTFT enters the saturation state. At this point, the current flowing through the driving transistor DTFT and the OLED is

$$I_{oled} =$$

$$\frac{1}{2}K(V_{gs} - V_{th})^2 = \frac{1}{2}K(V_c - V_{th})^2 = \frac{1}{2}K\left(\sqrt{\frac{2V_{data}}{K}} + V_{th} - V_{th}\right)^2 = V_{data}.$$

[0040] It can be seen that the driving current I_{oled} is not related to the threshold voltage of the driving transistor DTFT. Therefore, the drift of the threshold voltage of the driving transistor DTFT would not influence the drain current, i.e. the driving current I_{oled} of the pixel circuit, such that the OLED displays stably, improving the uniformity of the display brightness and improving the display quality dramatically.

Second Embodiment

[0041] The embodiment provides a display apparatus comprising the above pixel circuit. In particular, the display apparatus comprises multiple pixel unit arrays. Each pixel unit is corresponding to any pixel circuit in the above embodiments. Since the pixel circuit compensates the threshold voltage drift of the driving transistor, the OLED displays stably, improving the uniformity of the display brightness and improving the display quality dramatically.

[0042] The above implementations are only for illustrating the present disclosure and in no way limit the scope of the present disclosure. It will be obvious that those skilled in the art may make various modifications and variations without departing from the spirit and scope of the present disclosure. Such equivalent technical solutions also fall within the scope of the present disclosure.

1. A pixel driving circuit comprising an electroluminescent element, a driving transistor, a switch unit, a charging unit, a compensation unit, a cutoff unit and a storage unit, wherein

the switch unit is configured to control data voltage writing of a data line, a first terminal of the switch unit is connected to the data line, and a second terminal of the switch unit is connected to a first terminal of the charging unit, a first terminal of the compensation unit, a first terminal of the storage unit and the gate of the driving transistor respectively;

the charging unit is configured to control a driving power source to charge the storage unit, and a second terminal of the charging unit is connected to the driving power source and a first terminal of the electroluminescent element respectively;

the cutoff unit is configured to cut off electric connection between the electroluminescent element and the source of the driving transistor, a first terminal of the cutoff unit is connected to a second terminal of the electroluminescent element, and a second terminal of the cutoff unit is connected to the source of the driving transistor;

the compensation unit is configured to pre-store a threshold voltage of the driving transistor into the storage unit, and a second terminal of the compensation unit is connected to the source of the driving transistor; and

the drain of the driving transistor and a second terminal of the storage unit are grounded.

2. The pixel driving circuit according to claim 1, wherein the electroluminescent element is an organic light emitting diode (OLED), the storage unit is a storage capacitor, the switch unit is a first transistor, the charging unit is a third transistor, the compensation unit is a second transistor, and the cutoff unit is a fourth transistor;

the gate of the first transistor is connected to a first scan line, the source of the first transistor is connected to the data line, and the drain of the first transistor is connected to the first terminal of the storage capacitor, the source of the second transistor, the drain of the third transistor and the gate of the driving transistor respectively;

the gate of the third transistor is connected to a second scan line, and the source of the third transistor is connected to the driving power source and the first terminal of the OLED respectively;

the gate of the fourth transistor is connected to a control signal line, the source of the fourth transistor is connected to the second terminal of the OLED, and the drain of the fourth transistor is connected to the source of the driving transistor and the drain of the second transistor respectively; and

the gate of the second transistor is connected to the first scan line.

3. The pixel driving circuit according to claim 2, wherein all the transistors have a same channel type.

4. The pixel driving circuit according to claim 3, wherein all the transistors are N channel thin film transistors (TFTs); the first terminal of the OLED is the anode of the OLED, the anode of the OLED is connected to the high level output terminal of the driving power source, and the second terminal of the OLED is the cathode of the OLED.

5. The pixel driving circuit according to claim 3, wherein all the transistors are P channel thin film transistors (TFTs); the first terminal of the OLED is the cathode of the OLED, the cathode of the OLED is connected to the low level output terminal of the driving power source, and the second terminal of the OLED is the anode of the OLED.

6. A display apparatus comprising a pixel driving circuit according to claim 1.

7. A driving method of a pixel driving circuit, comprising the following steps of:

applying a control signal at a second scan line to control a third transistor to be turned on, applying a control signal at a first scan line and a control signal line to control a first transistor, a second transistor and a fourth transistor to be turned off, and a driving power source charging a storage capacitor;

applying a control signal at the first scan line to control the first transistor and the second transistor to be turned on, applying a control signal at the second scan line to control the third transistor to be turned off, and writing a threshold voltage of a driving transistor and a data voltage at a data line into the storage capacitor; and

applying a control signal at a control signal line to turn on the fourth transistor, applying a control signal at the first scan line to control the first transistor and the second transistor to be turned off, and driving the OLED to emit light by voltage stored in the storage capacitor.

8. The display apparatus according to claim 6, wherein the electroluminescent element is an organic light emitting diode (OLED), the storage unit is a storage capacitor, the switch

unit is a first transistor, the charging unit is a third transistor, the compensation unit is a second transistor, and the cutoff unit is a fourth transistor;

the gate of the first transistor is connected to a first scan line, the source of the first transistor is connected to the data line, and the drain of the first transistor is connected to the first terminal of the storage capacitor, the source of the second transistor, the drain of the third transistor and the gate of the driving transistor respectively;

the gate of the third transistor is connected to a second scan line, and the source of the third transistor is connected to the driving power source and the first terminal of the OLED respectively;

the gate of the fourth transistor is connected to a control signal line, the source of the fourth transistor is connected to the second terminal of the OLED, and the drain of the fourth transistor is connected to the source of the driving transistor and the drain of the second transistor respectively; and

the gate of the second transistor is connected to the first scan line.

9. The display apparatus according to claim 8, wherein all the transistors have a same channel type.

10. The display apparatus according to claim 9, wherein all the transistors are N channel thin film transistors (TFTs); the first terminal of the OLED is the anode of the OLED, the anode of the OLED is connected to the high level output terminal of the driving power source, and the second terminal of the OLED is the cathode of the OLED.

11. The display apparatus according to claim 9, wherein all the transistors are P channel thin film transistors (TFTs); the first terminal of the OLED is the cathode of the OLED, the cathode of the OLED is connected to the low level output terminal of the driving power source, and the second terminal of the OLED is the anode of the OLED.

* * * * *

专利名称(译)	像素驱动电路，其驱动方法和显示装置		
公开(公告)号	US20150084843A1	公开(公告)日	2015-03-26
申请号	US14/368408	申请日	2013-10-18
[标]申请(专利权)人(译)	京东方科技集团股份有限公司 成都京东方光电科技有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司 成都京东方光电科技有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司. 成都京东方光电科技有限公司.		
[标]发明人	LI TIANMA LI FAN		
发明人	LI, TIANMA LI, FAN		
IPC分类号	G09G3/32		
CPC分类号	G09G2330/00 G09G3/3258 G09G3/3208 G09G3/3225 G09G3/3233 G09G2300/0819 G09G2300/0842 G09G2300/0861 G09G2310/0262 G09G2320/0233 G09G2320/043 G09G2320/045 H01L27/3262		
优先权	201310329899.7 2013-07-31 CN		
其他公开文献	US9548024		
外部链接	Espacenet USPTO		

摘要(译)

提供一种像素驱动电路，其驱动方法和显示装置。像素驱动电路包括电致发光元件，驱动晶体管，开关单元，充电单元，补偿单元，切断单元和存储单元。像素驱动电路首先通过充电单元对存储电容充电，然后采用先充电和后放电的方法使存储电容中的电压快速匹配驱动电流，以快速补偿阈值电压 V_{th} 。因此，驱动晶体管提供的驱动电流不受阈值电压 V_{th} 的影响，从而提高了驱动电流的一致性，提高了亮度的均匀性。

